1st National Conference on EW Systems & Technologies

EWST 2023

Patrons :

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Schedule :

21 Feb 2023 : Pre-Conference Workshop

22 Feb 2023 : Inagural Session, Invited Talks, Technical Sessions, Cultural Night, Conference dinner

23 Feb 2023 : Invited Talks, Technical Sessions, Valedictory Function

Venue :

Dr. V. Narayana Rao Auditorium,
Defence Electronics Research Laboratory (DLRL),
Chandrayanagutta, Hyderabad - 500 005.

1st National Conference on EW Systems & Technologies

The first National Conference on Electronic Warfare Systems & Technologies (EWST - 2023) is being organized by Defence Electronics Research Laboratory (DLRL), a premiere EW Laboratory of Defence Research & Development Organization (DRDO). The Conference has active support from patrons of DRDO. This EW Conference will provide an excellent platform for Armed Forces, Industry experts, Scientist, Users & Engineers for exchanging knowledge, sharing experience to collaborate and reap mutual benefits. The One-day Pre-Conference workshop and Two-day Conference is visualised and designed to meet the expectations of EW Professionals.

Theme of the Conference
Emerging Trends & Challenges in EW Systems & Technologies

Pre-Conference Workshop on
EW Systemes Architectures, Techniques & Technologies

Topics :

- Advances in EW Antennas & Radomes
- Latest trends in EW Receivers & RF Sub-systems
- Advanced Signal Processing & DF Techniques in EW
- EW System Modelling and Threat Simulation
- Challenges in Thermal & Structural Design in EW
- Installation, Testing & Field Evalutaion of EW Systems

- Certification aspects of EW Systems
- Joint Services EW Operations & Challenges
- Satellite & Missile EW
- Application of AI & ML in EW
- Cyber Security Challenges in EW

Instructions to Authors

Authors who wish to present the papers should submit an Abstract/Summary of the paper not more than 500 words in MS Word format. The Abstract should begin with the Title of the paper, Name(s), Affiliations, Full Adress for correspondense and Email Address. The document should be emailed to ewst2023.dlrl@gov.in. Last date for submitting the abstract is 23 December 2022. Full paper should be submitted after receiving information about acceptance of abstract in the prescribed format before 15 January 2023. Authors presenting the paper durig the Conferece are required to register by paying the Delegate registration fee on or before 10 February 2023. Authors are requested to submit abstract as per the template provided.

The Title of The Paper Is In Font Size 20 And In Bold With 2 To 3 Lines, In Title Case

Author 1, Author 2 and Author 3 Affiliation, Affiliation, Affiliation, Corresponding author E-mail ID.

Abstract:

The detailed abstract should be elaborately presented in more than 500 words.

The details of technical idea / design / implementations along with simulation /measured data to be given in the abstract.

Below the title, mention name(s) of Author and Co-authors. If authors have different affiliations followed by the affiliations with center aligned.

Email ID: of corresponding author, in bold, below affiliation (single lines spacing), center aligned

Paper size: A4 with 1" margin on all sides.

Font: Times New Roman

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Interline spacing: Throughout the text should be "SINGLE"

Strictly No page numbering. No header / footer such as company name /logo

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EWST 2023 Schedule

21 February 2023	Pre-Conference Workshop
22 February 2023 -	Inagural Session, Invited Talks, Technical Sessions on Antennas, Receivers, MW and Digital Processing techniques & Algorithms, Cultural Night & Conference Dinner
23 February 2023 -	Invited Talks, Technical Sessions on ECM & Space EW Systems, EW System Ground, Air & Navy, QA & Testing aspects, Valedictory Function

EWST 2023 Important Dates

23 December 2022	Start date to receive Abstracts/Summary of Technical Papers (<=500 Words)
31 December 2022	Acceptance of Abstract
15 January 2023	Last date to submit Full Technical Paper
31 January 2023	Acceptance of Full Paper
31 January 2023 to	
10 February 2023	Registration & Payment

Delegates Registraton

Registration Fee in Rs		Workshop	Conference
Students / Research Scholars	:	1000.00	2000.00
Govt / DPSU	:	2000.00	4000.00
Others	:	3000.00	6000.00

Registration Process

• Format for Delegate Registration will be available at DRONA & Internet Website

• All Conference and Pre-Conference workshop delegates will be given entry pass

Payment Details

(All payments are to be made Online and a copy of Payment receipt to be sent to ewst2023.dlrl@gov.in)

Account Name	: Director DLRL Public Fund Account
Account No.	: 10326261120
IFSC Code	: SBIN0003026
Bank	: SBI, Chandrayangutta

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Registration Form

Date of Workshop: *21 Feb 2023* **Date of Conference:** *22 &23 Feb 2023*

Venue: *V Narayana Rao Auditorium, DLRL, Chandrayangutta line, Hyderabad -500005*

NAME (Mr. /Ms/ Mrs. / Miss / Dr / Prof)*		**DESIGNATION***	
ORGANIZATION* WITH ADDRESS		**EMAIL***	
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	DATE OF PAYMENT		**TRANSACTION ID**	

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Note:

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3. On spot registration is not allowed due to security reasons.

4. Limited accommodation is available on first come first serve basis.

5. I have read all information provided in conference Brochure.

Date: ……………………………….

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Instructions to Authors for Full paper Submission to EWST 2023

The following instructions are prepared to be as author friendly as possible. Use only MS Word Package for preparing your final paper. If you are sending a pdf file, Word File also should accompany.

1 **Margins**: Top= 0.8", Bottom= 0.9", Left =0.6", Right= 0.6", Gutter Position= 0.2 from left
2 Paper Size = A4, Orientation: Portrait

3 **Font** Type: Times New Roman (Throughout the Paper)

4 **Title** of the Paper: - Font Size 24, Bold, Centre Aligned, Title Case
Not more than 3 lines, Interline spacing "single". Avoid abbreviations as far as possible
5 **Author(s)**: Font size 11. Below the title leave 2 line spacing and mention name(s) of Authors with corresponding author as first name, Co-Authors names to follow. If authors have different affiliations, then at the end of author's name superscript numbers 1, 2, 3, etc., Centre Aligned
6 **Affiliations**: Font Size 10, should come below author(s) name(s), with complete postal address of corresponding author and organization of co-authors (corresponding to the superscripts entered against authors names, center aligned)
7 **e-mail ID:**- (Font size 10), of corresponding author, in bold, below affiliations (single line spacing), centre aligned
8 Leave 2 line spacing and the Format is Double column from this point onwards. Make 0.25" as inter-column spacing.

I ABSTRACT OF THE PAPER:

Type "Abstract", left aligned, in bold and in italics. Font size 9
The Abstract starts next line as a paragraph. The Abstract should be worded such that it arouses interest and enthusiasm and conveys very briefly the essence of the paper. At the end, leaving single line spacing, key words should appear to assist indexing/ abstracting. Abstract should be restricted to 60 words (+/- 5). Select the Full Abstract and make it in italics. (Font Size 9). Justify.

II THE FULL TEXT

Now the Full Text follows (Font Size **10),** starting with the Heading "INTRODUCTION", centre aligned, Bold. All **Headings** are in Small Caps with first letter of the words in Font 11. Headings can be numbered in ROMAN Numerals. Leave 3 spaces between the number and the heading. Do not number ACKNOWLEDGEMENT and REFERENCES.

All **subheadings** (Font 8) are left aligned and in Italics, with numbering in Arabic Numerals. Leave 3 spaces after the number for the subheading.

IMPORTANT

a) Restrict the total **No. of Pages** to 6 to 8 only (Including References and Bio of Authors.), highlighting the achievements/contributions. Basics of the idea presented need not be included in the paper. The Author can mention that "additional info in the form of Photographs, Graphs etc are available on request" at the end of the paper. Please note that too lengthy papers are liable to be rejected.
b) References should be exhaustive and explicitly stated. With consecutive numbering as [1],[2], etc
c) INTERLINE SPACING THROUGH OUT THE TEXT SHOULD BE "SINGLE"
d) Strictly **No page numbering** please. **No header/ footer such as Company name/ logo** etc.
e) Preferably, Figures **and tables** should be numbered and placed suitably at the top or bottom of a column. Figure captions/ table names should be in **Font 8**

f)REFERENCES: - Mention (Use Font Size 8) all the references already indicated by [1],[2] etc. in sequence with full details as to author(s), Title of the paper / publication/ Vol/ page nos/Year of publication etc., at the end of the paper The first letter of the referred Title of the paper should only be in capitals.

g) Bio data of Authors: - Should follow immediately after reference. Include a stamp size photo of the author(s) with brief resume of all authors not exceeding one page /paper. Black& White photos (digitized) are preferred. The Bio data of the author presenting paper should be given first and the same will be used to introduce him / her during presentation. BIO DATA SHOULD FORM PART OF THE PAPER AND NOT SENT SEPARATELY. Biodata should be restricted to not more than 50 words.

*Note: - We Acknowledge the Sample paper of IEEE titled "**Preparation of Papers in Two Column Format for Conference Proceedings Sponsored by IEEE**" as a reference for the above guidelines.*
SAMPLE PAPER FORMAT FOLLOWS (Everything coming after this arrow is fiction. Any resemblance to anything actual is incidental)

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AK Singh[1], Ramesh L[2] and Gopal KP[3]

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Abstract:

Rytweuitruiy wueytuo ewt tuioqwt uiotwoi ui uqwuutw uiqwt t uqwt tuiqw uqwiuqw gdgdoASGDO GAISDG agdgsg Ogdgiasdg GASDUIGDGDdiud dgfhdsgfg fggjsdgfjgh dsggjasgh dgf ghdsf df ldgfl l lgdfl gfhdgfljsd gflsdfjlsdghflfg lhasldkvb xcbvbvmc vlkjsdgsdgf bl gfklsdb ljksdgflsg flsdflsdfl bcbc sjkdgfklsfkdsjf ksbc kjdfjff lflasdgfk;jshgl sjhlkdsjf sdgfdsl glsdgfl ldf lsdfl kdsgfkl dsfl sdflsdf lkadsgfls dlakdsjg fldsfladsg fdhhhfkhfl lasjkdgf ldsgflkasjgfl lskadgfkl sdflkasgdf lkasdf lkgf ksldhfk

Key Words: mmm, Spectrum Analyzer, mmmmm, jjjjjjjjjj

I INTRODUCTION:

Tuiwet boewuitoib wioeut iqweut pwuietrweu wieutyr wweuitrqioweur grfiqwuerdgfkladsjfl asf ioeugo wegroiew qwiegur wqegure [1], vipwpurwe wqegirgqwe ioqwegioqewugri owgiqwoegriwoqegr wqegriegewgri qweiugriqweugr iqewugriquew iurqwiegr ioqewgr

II. HEADING 1

Sdaf dsgfo soygfsadgf ogfo ofgo oadsgf ofgo fgo ofasdgf afg adsgf ofoasgdfyo fgifgif isuadfsid fiasdgfi dgfiadsgf [2], isdgfiasd gfiiasdgf iadsgfgf idfgdi adsgf askdjfgsadgfk akdsjgfkadsjgf sdfgasdf asdgfks dhfhfkl adskjgfas fkadsjgf jkasdgfk adsjgfkasjdgfkjadsg fjkgasdk ajkldsgfjkdsagf jkladsgfjkasdf asjkdfjffdf

1 Subheading 1

The inclusion of this parameter allows for the asymmetric fall in Cgd (slower) and rise in Cgs (faster) as we increase Vds from 0V in the expression n.[3]

III. HEADING 2

Equation numbers are to be aligned right with the tabs or spaces as given the following example.

$$x=a+b+c \tag{1}$$
$$y=a-b+c \tag{2}$$

From eqn (1) and (2) it is obvious that

$$x+y = 2(a+c) \tag{3}$$

While considering the variables , when one is interested only in the sum (x+y) , then variable „b" has no relevance.

2 Subheading 2

ewtoqt egrquew gui gouiewrqieur giuqw gte rq gqr qwyefr cqy wekgc uiqge g gr cewggr lgrklqwegr kw gqwgrkjgrgr kh lqwgerhqweg glgrl hglqjkgrlhgr hj g lgrjghrjqgr lbkdgf,jasdgslglgfl dgfg fkhffffkhjfk sdagdjk assgsh sdsfjksh gbn sassssgsg rljg.dfgbfsdgf jkgfajkh gjfgbjkdsh gfjkshf

haskahsfkhfkg vmnvmnv vnv jhkj jhgkjhg hjv b hh hgh g jgjhgf h hhf vgfkg kfhv fh hfk f hfk kghf h khf khjh hhjh hjf hjhj jhjh gvh hvhjhh jhj,h ,h hjh hhh.

Fig.1

Figure 1: Fundamental frequencies observed at various bias points

CONCLUSION

The new model for MESFETs has been proved to have excellent convergence properties, making harmonic. The foundry engineers have approved the MMIC VCO

ACKNOWLEDGMENT

REFERENCES

[1] H. Harnal, A. Basu, S. K. Koul, R. K. Khatri, H. P. Vyas, and A. Kumar, "An improved model for GaAs MESFETs suitable for a wide bias range," *IEEE Microwave and Wireless Component Letters,* vol. 17, no. 1, pp. 52-54, Jan 07

[2] R. Gilmore and L. Besser, *Practical RF Circuit Design for Modern Wireless Systems*, Volume II Active Circuits and Systems, Artech House, 2002

[3] I.D. Robertson and S. Lucyszyn, *"RFIC and MMIC Design and technolog,y"* IEE Circuits, Devices And Systems Series 13, 2001

BIO DATA OF AUTHOR(S)

Photo 1

AK Singh g f j h a fjhgfjghggljgg fja h fgja gh gjk s ghgfgc ksljkasghlaslhl sh lkaj hka sshgg dfgdf g gsdfgdfgd g dfg ddf g hfdgfhgfh gfhkasl kasld laskdla kd la skdlkan asdlk as dklaskdla dnlaksndl ksdnlaksndla kfgdffsd fgdf

Ramesh L g f j h a fjhgfjghggljgg fja h fgja gh gjk s ghgfgc ksljkasghlaslhl sh lkaj hka sshgg dfgdf g gsdfgdfgd g dfg ddf g hfdgfhgfh gfhkasl kasld laskdla kd la skdlkan asdlk as dklaskdla dnlaksndl ksdnlaksndlak ffsdfgdf

Photo 2