



Template No.
CEMILAC_SYSGP_SAS_15

**SOFTWARE ACCOMPLISHMENT
SUMMARY
for <LRU/SYSTEM Name>
for
<Platform Name>**

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SOFTWARE ACCOMPLISHMENT SUMMARY for <LRU/SYSTEM Name>for <Platform name>			< Project/System Name>	
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	Name & Designation		Signature	
Prepared By	<Design Rep Name>, < Designation> <Agency Name>			
Reviewed By	<Project Leader Name>, <Designation> <Agency Name>			
	<AWG/QA HOD Name>, <Designation> <Agency Name>			
Approved By	<Project Leader Name>, <Designation> <Design Agency> <Officer_Name>, <Designation> RCMA <Name>			
<Design Firm Name & Address>				

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Disclaimer:

This document is a guidance document. Applicable section / table rows may be considered. Any additional details may be added. Any not applicable section/ table rows may be deleted. The template is very general and vary with process to process followed by Development Agency. The document may be fine-tuned with the TAA for finalization.

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Amendment History

Issue No.	Issue Date	Brief Description of Amendment	Change Request Ref.	Affected Pages	Affected Section	Change Effective From (Version/ Date)
001		Initial Issue	NA	NA	NA	Initial
002						

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1.0 INTRODUCTION

1.1 Purpose

1.2 Scope

1.3 Acronyms and Abbreviations

1.4 External Documents

Include references to standards, manuals, OEM documents etc.

1.5 Internal Documents

Supply references to the following documents regarding software

- a) Technical/ system/ functional specification
- b) ICD
- c) User/ Operation manual
- d) Aircraft integration/ Flight trial reports
- e) User feedback
- f) Any MoM relevant to software verification and certification

2.0 Software Identification

This document describes the certification details of the following software component(s) :

Sl.No.	CSCI Name	Software Version	Checksum

3.0 System Overview

This section provides an overview of the system, including a description of its functions and their allocation to hardware and software, the architecture, the processor(s) used, the hardware/software interfaces, and safety features. This section also describes any differences from the system overview in the PSAC.

4.0 Software Overview

Include in brief the description of software application for all CSCIs.

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4.1 Software Architectural Block Diagram

4.2 Processor 1

4.3 Processor 2

4.4 COTS software

4.5 Other programmable devices

5.0 Resource Utilization

Give details of available resource capacity and utilised capacity and the means of measuring each characteristic.

Program Flash Usage	:
Processor RAM Usage	:
NVRAM Usage	:
Flash Usage	:
FPGA/ CPLD/ PAL usage	:
Timing margin in Processor 1	:
Timing margin in Processor 2	:

6.0 Software Lifecycle Data

Give references to SRD, SDD, SVP, SVCP, SVR and SCI documents and revisions to these documents. Include Problem report and Change notice references.

7.0 Certification Considerations

Include details of certification considerations and if any difference to PSAC.

7.1 Certification Basis and Means of Compliance

Same table of objectives and means of compliance as in PSAC, with latest changes during the process, if any.

7.2 Software Conformity to Objectives

Table of objectives with compliance evidence document (mention document name and page numbers against each objective) and limitations/ deviations/ waivers, if any, in each of the compliance evidence. Bring out the observations during verification of each objective and whether they were closed.

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8.0 Additional Considerations

9.0 Change History

If applicable, this section includes a summary of software changes with attention to changes made due to failures affecting safety, and identifies changes from the software life cycle processes since the previous certification. Include details of all the SCNs.

10.0 Software Status

This section contains a summary of problem reports unresolved at the time of certification, including a statement of functional limitations.

11.0 Compliance statement:

This section includes a statement of compliance with this document and a summary of the methods used to demonstrate compliance with criteria specified in the software plans. This section also addresses additional rulings and deviations from the software plans, standards and this document.

<A table may be added to indicate compliance status of objectives, IV&V activities, SQA activities as per PSAC, SVP and SQAP>.

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