

**RECRUITMENT FOR THE POST OF JUNIOR RESEARCH FELLOWSHIP IN**  
**DRDO YOUNG SCIENTISTS' LABORATORY FOR QUANTUM TECHNOLOGIES**  
**(DYSL-QT), PUNE**

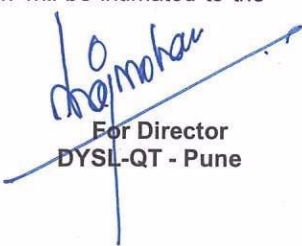
Lab invites applications from candidates having strong desire to pursue research in the area of innovative technology and possessing qualifications as under, for the position of Junior Research Fellowship (JRF). Initially, the fellowships shall be for a period of two years (which can be extendable for further two years as per rules in force).

<b><u>S. No.</u></b>	<b><u>Name of post</u></b>	<b><u>No. of Vacancies</u></b>	<b><u>Upper Age Limit</u></b>	<b><u>Minimum eligibility criteria</u></b>	<b><u>Desirable Qualification</u></b>
1.	JRF	03	28 years as on the last date of receipt of application. The upper age limit shall be relaxable to candidates from SC, ST and OBC (NCL) category as per Govt. rules in vogue.	B.E/B.Tech Degree in Electronics & Communication Engg / Electronics/Electrical & Electronics/Electronics & Telecommunication in 1 <sup>st</sup> Division with Valid NET/GATE qualification (OR) ME/M.Tech Degree in Electronics & Communication / Electronics/Electrical & Electronics/Electronics & Telecommunication Engg in 1 <sup>st</sup> Division both at Graduate and Postgraduate level.	1) Verilog coding, experience of working with FPGA specially ZYNQ architecture. 2. Hands-on-experience working on FPGAs, bare-metal programming, Linux level programming. 3. Experience in Verilog/ VHDL based programming, high speed communication interfaces like gigabit transceivers, Ethernet etc., error correction protocols and its implementation on FPGA. 4. C, Python based programming, MATLAB. 5. Proficiency in Communication and DSP algorithms. 6. Having knowledge of working in embedded system and FPGA based design. 7. Exposure to Quantum Technologies

- The detailed advertisement with application form and other specimen formats is available in the website [www.drdo.gov.in](http://www.drdo.gov.in) (> What's New > Application for the post of JRF at DYSL-QT, Pune). Interested candidates can also obtain the application form and other details through email: [dyslqtdrdo@gmail.com](mailto:dyslqtdrdo@gmail.com).
- The duly filled-in application and self-attested copies of educational qualifications and experience should reach:  

**The Director,  
DRDO Young Scientist Laboratory-Quantum Technology  
Defence Research and Development Organisation (DRDO)  
Hall No. 1, Ground Floor, Vigyan Upakendra,  
DIAT Campus, Girinagar, Pune – 411025, Maharashtra**

within 30 days from the date of publication of the advertisement in Employment News. The envelope containing application should be super scribed "**Application for JRF**" at the top left corner. In addition, they may send a soft copy of the application to [dyslqtdrdo@gmail.com](mailto:dyslqtdrdo@gmail.com).
- Mode of selection shall be by Interview. The Date, time, venue and mode of interview will be intimated to the shortlisted candidates by post and email.

  
For Director  
DYSL-QT - Pune